

respective operations and/or exhibiting respective functionalities, and/or the respective devices (and/or parts thereof) may have functions for performing respective operations and/or exhibiting respective functionalities.

**[0083]** When in the subsequent description it is stated that the processor (or some other means) is configured to perform some function, this is to be construed to be equivalent to a description stating that a (i.e. at least one) processor or corresponding circuitry, potentially in cooperation with computer program code stored in the memory of the respective apparatus, is configured to cause the apparatus to perform at least the thus mentioned function. Also, such function is to be construed to be equivalently implementable by specifically configured circuitry or means for performing the respective function (i.e. the expression “processor configured to [cause the apparatus to] perform xxx-ing” is construed to be equivalent to an expression such as “means for xxx-ing”).

**[0084]** In its most basic form, according to exemplary embodiments of the present invention, the apparatus **10** or its processor **11** is configured to perform obtaining (e.g. cellular type) access-related network selection information with respect to a network or a network technology type, obtaining (e.g. non-/cellular type) routing-related network selection information with respect to traffic type based routing information, and performing traffic control based on a combination of the access-related network selection information and the routing-related network selection information.

**[0085]** Accordingly, the apparatus **10** may comprise respective means for obtaining and means for performing traffic control.

**[0086]** As outlined above, in enhanced forms, the apparatus **10** may comprise one or more of respective means for selecting a network or a network technology type, means for deciding a route for routing of traffic of a specified traffic type, means for transmitting traffic of the specified traffic type, and means for receiving the (e.g. cellular type) access-related network selection information and/or the (e.g. non-/cellular type) routing-related network selection information.

**[0087]** For further details regarding the operability/functionality of the individual apparatuses, reference is made to the above description in connection with any one of FIGS. **1** to **5**, respectively.

**[0088]** According to exemplarily embodiments of the present invention, the processor **11/21**, the memory **12/22** and the interface **13/23** may be implemented as individual modules, chips, chipsets, circuitries or the like, or one or more of them can be implemented as a common module, chip, chipset, circuitry or the like, respectively.

**[0089]** According to exemplarily embodiments of the present invention, a system may comprise any conceivable combination of the thus depicted devices/apparatuses and other network elements, which are configured to cooperate as described above.

**[0090]** In general, it is to be noted that respective functional blocks or elements according to above-described aspects can be implemented by any known means, either in hardware and/or software, respectively, if it is only adapted to perform the described functions of the respective parts. The mentioned method steps can be realized in individual functional blocks or by individual devices, or one or more of the method steps can be realized in a single functional block or by a single device.

**[0091]** Generally, any method step is suitable to be implemented as software or by hardware without changing the idea

of the present invention. Such software may be software code independent and can be specified using any known or future developed programming language, such as e.g. Java, C++, C, and Assembler, as long as the functionality defined by the method steps is preserved. Such hardware may be hardware type independent and can be implemented using any known or future developed hardware technology or any hybrids of these, such as MOS (Metal Oxide Semiconductor), CMOS (Complementary MOS), BiMOS (Bipolar MOS), BiCMOS (Bipolar CMOS), ECL (Emitter Coupled Logic), TTL (Transistor-Transistor Logic), etc., using for example ASIC (Application Specific IC (Integrated Circuit)) components, FPGA (Field-programmable Gate Arrays) components, CPLD (Complex Programmable Logic Device) components or DSP (Digital Signal Processor) components. A device/apparatus may be represented by a semiconductor chip, a chipset, or a (hardware) module comprising such chip or chipset; this, however, does not exclude the possibility that a functionality of a device/apparatus or module, instead of being hardware implemented, be implemented as software in a (software) module such as a computer program or a computer program product comprising executable software code portions for execution/being run on a processor. A device may be regarded as a device/apparatus or as an assembly of more than one device/apparatus, whether functionally in cooperation with each other or functionally independently of each other but in a same device housing, for example.

**[0092]** Apparatuses and/or means or parts thereof can be implemented as individual devices, but this does not exclude that they may be implemented in a distributed fashion throughout the system, as long as the functionality of the device is preserved. Such and similar principles are to be considered as known to a skilled person.

**[0093]** Software in the sense of the present description comprises software code as such comprising code means or portions or a computer program or a computer program product for performing the respective functions, as well as software (or a computer program or a computer program product) embodied on a tangible medium such as a computer-readable (storage) medium having stored thereon a respective data structure or code means/portions or embodied in a signal or in a chip, potentially during processing thereof.

**[0094]** The present invention also covers any conceivable combination of method steps and operations described above, and any conceivable combination of nodes, apparatuses, modules or elements described above, as long as the above-described concepts of methodology and structural arrangement are applicable.

**[0095]** In view of the above, there are provided measures for non-conflicting traffic control with different types of network selection information. Such measures exemplarily comprise performing traffic control based on a combination of (e.g. cellular type) access-related network selection information with respect to a network or a network technology type with (e.g. non-/cellular type) routing-related network selection information with respect to traffic type based routing information. Such measures are exemplarily, but not exclusively, applicable in the context of coexisting cellular and non-cellular networks or network technology types providing connectivity to a transport network.

**[0096]** The measures according to exemplary embodiments of the present invention may be applied for any kind of network environment, such as for example for communication systems in accordance with any related standards of 3GPP